



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):	Robert W. Dixon		
Serial No.:	10/731,285	Examiner:	Kim T. Huynh
Filed:	December 9, 2003	Group Art Unit:	2112
Title:	INTERFACE BUS OPTIMAZATION FOR OVERLAPPING WRITE DATA		
Docket:	STL 11666/S104.12-0137		

**POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST  
(REVOCATION OF PRIOR POWERS)**

As assignee of record of the entire interest of the above identified application,  
REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given are hereby revoked and

**NEW POWER OF ATTORNEY**

the practitioners/patent agents associated with

Customer Number 27,365

are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith.

**CHANGE OF ATTORNEY'S/AGENT'S ADDRESS IN APPLICATION**

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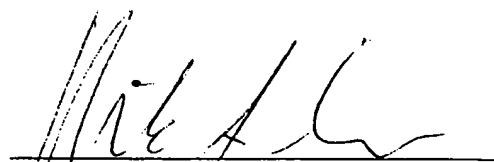
**STATEMENT UNDER 37 CFR 3.73(b)**

Seagate Technology LLC states that it is the Assignee of Entire Interest in the patent application/patent identified above by virtue of an Assignment from the inventor(s) of the patent application/patent identified above. A copy of the Assignment is attached and/or was recorded in the Patent and Trademark Office at Reel , Frame . The undersigned (whose title is supplied below) is empowered to sign this statement on behalf of the Assignee.

Respectfully submitted,

SEAGATE TECHNOLOGY LLC  
(Assignee of Entire Interest)

1-12-06  
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